Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	125	memory adj3 stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:27
S2	11	("6877079" "6772262" "6177807" "6834014" "6625687" "6442644").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/18 16:47
S3	20	("5357621" "5991850" "6016282" "6034878" "6088774" "6109929" "6115318" "6131149" "5650757" "6128748").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:05
S4	8	memory near3 stub near3 configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:05
S5	9	memory near3 stub near3 (configuration arrangement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:07
S6	18	memory with stub near3 (configuration arrangement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:08
S7	852	stub near3 (configuration arrangement)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:08
S8	9	stub near3 (configuration arrangement) same $3ram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:09
S9	3636	writ$3 same memory same first near3 (clock rate frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:36

S10	1917	writ$3 with memory with first near3 (clock rate frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:36
S11	2911	read$3 with memory with first near3 (clock rate frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:37
S12	2471	generat$3 with memory with second near3 (clock rate frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:37
S13	2636	read$3 with memory with second near3 (clock rate frequency)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:37
S14	378	S10 and S11 and S12 and S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:43
S15	176	S14 and $3ram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:39
S16	790	S12 same S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:39
S17	332	S10 and S11 and S16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:39
S18	144	S17 and $3ram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:40
S19	60	S17 and dram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:40

S20	5	S17 and stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:41
S21	6	S16 and stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:42
S22	6	S14 and stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:42
S23	277	S10 same S11 same S12 same S13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:43
S24	7	S23 same dram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:45
S25	2	S23 same sdram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:47
S27	5	S23 and stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:47
S28	0	S23 same sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:47
S29	12	S23 and sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:53
S30	51	S23 and dram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 14:08

S31	50	S30 not S8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 13:53
S32	44	S30 not S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 14:09
S34	26	S23 and sdram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 14:08
S35	24	S34 not S25	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 14:50
S36	0	S23 same dimm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 14:50
S37	1	S23 and dimm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/19 14:50
S38	22531	address same clock same control same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:18
S39	225	S38 and stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:19
S40	8	S38 same stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:13
S41	6	S38 and stub near5 configur$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:19

S42	6399	address with clock with control with memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:18
S43	64	S42 and stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:19
S44	5	S42 and stub near5 configur$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:19
S45	5	S42 same stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/20 09:19
S46	14	memory near3 modules with different near3 phase$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 15:46
S47	70841	different near3 phase$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 15:46
S48	1223	different near3 phase$1 with advantage$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 15:46
S49	60	generat$3 near5 different near3 phase$1 with advantage$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 15:46
S50	31	generat$3 near5 different near3 phases with advantage$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 15:58
S51	817	different near3 phases with reduc$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:00

S52	41	different near3 phases near3 clock with reduc$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:21
S53	15	propagation adj2 delay with equal with data adj2 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:50
S54	4	capacitor with capacitance near5 compensat$3 with capacitive adj2 loading	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:51
S55	9	capacitance near5 compensat$3 with capacitive adj2 loading	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:57
S56	398	capacitor same memory adj2 module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:57
S57	1	capacitor same memory adj2 module same junction with return	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 16:58
S58	4	capacitor same memory adj2 module same return	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:00
S59	192	capacitor with junction with return	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:01
S60	19	capacitor with junction with return adj2 line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/27 17:01
S63	352	(onello near3 mills).att. and samsung.asn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 10:46

S64	3	(onello near3 mills).att. and samsung.asn. and motherboard. clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 10:47
S65	109	(onello near3 mills).att. and samsung.asn. and memory.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 10:48
S66	9	(onello near3 mills).att. and samsung.asn. and (memory near3 module).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 10:59
S67	37	memory near3 modules with (indpendent separate) near3 lines	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 11:00
S68	0	memory near3 modules with (indpendent separate) near3 clock near3 lines	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 11:00
S69	5697	generat$3 with read near3 clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:40
S70	2661	S69 same (dram sram memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:39
S71	420	S70.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:38
S72	136	S69 same (dram sram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:39
S73	4	S72.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:39

S74	1544	generat$3 with read near3 clock with output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:40
S75	296	S74.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:40
S76	238	S75 and (dram sram memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:40
S77	56	generat$3 with read near3 clock with output with bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:49
S78	13	S77.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 14:41
S79	19	generat$3 near5 read near3 clock with output near5 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:25
S80	1432	advantage$5 with stub	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:30
S81	15	S80 same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:26
S82	4	advantage$5 with stub near3 configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:31
S83	14	reduc$4 with stub near3 configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:32

S84	0	decreas$3 with stub near3 configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:32
S85	11	increas$3 with stub near3 configuration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/28 16:32
S86	1	clock near5 lines near3 cross$3 with motherboard	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:29
S88	68	clock near5 lines near3 cross$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:29
S89	59	clock near3 lines near3 cross$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:34
S90	13	clock near3 lines near3 cross$3 same reduc$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:34
S91	13	clock near3 lines near3 cross$3 same reduc$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:35
S92	2	clock near3 lines near3 cross$3 same decreas$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:36
S93	7	clock near3 lines near3 cross$3 same increas$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:38
S94	167	signal near3 lines near3 cross$3 same increas$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:38

S95	10	signal near3 lines near3 cross$3 same increas$4 same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:52
S96	2	clock adj2 line near5 dummy adj2 load	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/29 13:53
S97	0	dummy adj2 load same double adj2 side$1 same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:53
S98	1	dummy adj2 load same double adj2 side$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:53
S99	212	memory near5 double adj2 side$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:54
S100	0	memory near5 double adj2 side$1 same buffer same lines	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:55
S101	2	memory near5 double adj2 side$1 same lines	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:56
S102	0	memory near5 double adj2 side$1 same sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:56
S103	55	memory near5 double adj2 side$1 and (sram dram)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 08:56
S104	2822	double adj2 sided with advantage$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 10:55

S105	47	S104 same memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 10:55
S106	43	S105 not capacitor$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/30 10:55